San José State University
Electrical Engineering Department
CURN0 0 9.96 251.88 710.76 Tm 1 TJ /TT0 1 Tf 0 Tc 0 Tw 17.65

20%

TEACHING RESPONSIBILITIES
Semiconductor Devices and Circuits, VLSI technology, Electromagnetics, Senior design project advising, MSEE project advising

EDUCATION
Degree

	Year
Faculty Summer Research Fellow, US Airforce Research lab in MA	1997
Stanford Nanofabrication facility, Summer Research Fellow	2009

CONSULTING AND PATENTS

Description	**Year**
None	

PROFESSIONAL REGISTRATION, LICENSING AND CERTIFICATION

State	**Field**
None	

HONORS, GRANTS AND AWARDS
National Science Foundation, CAREER Award, 1996

MEMBERSHIP IN PROFESSIONAL SOCIETIES
IEEE Senior member: Chair, IEEE Santa Clara Valley Education Society 2004-2008; IEEE Education Society, IEEE Electronic Device Society, IEEE Circuit and System Society, IEEE Communication Society